

METHOD AND APPARATUS FOR A LOW WRITE CURRENT MRAM HAVING A WRITE MAGNET

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ABSTRACT OF THE DISCLOSURE

An invention is provided for a low write current MRAM. Each MRAM cell includes a word line and a bit line. A magnetic device is disposed at the intersection of the word line and the bit line. Disposed at either end of the magnetic device is a writing magnet. The pair of writing magnets switches a magnetic alignment of the magnetic
10 device during a write operation. In aspect, the pair of writing magnets and the magnetic device can be aligned along a long axis of the memory cell, which generally is not aligned with either the word line or the bit line.